



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 7

Complete if Known

Application Number	10/027,589
Filing Date	December 19, 2001
First Named Inventor	Julie Ann WARD
Art Unit	2142
Examiner Name	Thong H. VU
Attorney Docket Number	10019570-1

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
1	1A	US- 2002-0083159-A1	06-27-2002	Ward et al.	
1	1B	US- 2002-0091804-A1	07-11-2002	Ward et al.	
1	1C	US- 2002-0120770-A1	08-29-2002	Parham et al.	
1	1D	US- 2002-0156828-A1	10-24-2002	Ishizaki et al.	
1	1E	US- 2002-0188732-A1	12-12-2002	Buckman et al.	
1	1F	US- 2003-0065758-A1	04-03-2003	O'Sullivan et al.	
1	1G	US- 2003-0144822-A1	07-31-2003	Peh et al.	
1	1H	US- 2003-0145294-A1	07-31-2003	Ward et al.	
1	1I	US- 2005-0021583-A1	01-27-2005	Andrzejak et al.	
1	1J	US- 2005-0021831-A1	01-27-2005	Andrzejak et al.	
1	1K	US- 2005-0033844-A1	02-10-2005	Andrzejak et al.	
1	1L	US- 5,138,657-A	08-11-1992	Colton et al.	
1	1M	US- 5,245,609-A	09-14-1993	Ofek et al.	
1	1N	US- 5,426,674-A	01-20-1995	Nemirovsky et al.	
1	1O	US- 5,581,689-A	12-03-1996	Slominski et al.	
1	1P	US- 5,598,532-A	01-28-1997	Liron	
1	1Q	US- 5,649,105-A	07-15-1997	Aldred et al.	
1	1R	US- 5,651,005-A	07-22-1997	Kwok et al.	
1	1S	US- 5,793,362-A	08-11-1998	Matthews et al.	

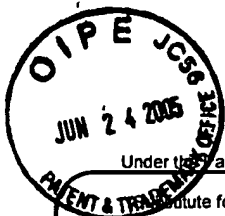
FOREIGN PATENT DOCUMENTS						
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
1	1T	WO-96/17458-A	06-06-1996	British Telecommunications Public Limited Company		

Examiner Signature		Date Considered	10/13/05
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Sheet 2 of 7

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u	2A	US- 5,805,578-A	09-08-1993	Stirpe et al.	
u	2B	US- 5,815,402-A	09-29-1998	Taylor et al.	
u	2C	US- 5,838,919-A	11-17-1998	Schwaller et al.	
u	2D	US- 5,987,517-A	11-16-1999	Firth et al.	
u	2E	US- 6,003,037-A	12-14-1999	Kassabgi et al.	
u	2F	US- 6,031,984-A	02-29-2000	Walser	
u	2G	US- 6,038,219-A	03-14-2000	Mawhinney et al.	
u	2H	US- 6,047,199-A	04-04-2000	DeMarco	
u	2I	US- 6,052,360-A	04-18-2000	Rogers	
u	2J	US- 6,108,782-A	08-22-2000	Fletcher et al.	
u	2K	US- 6,141,355-A	10-31-2000	Palmer et al.	
u	2L	US- 6,157,645-A	12-05-2000	Shobatake	
u	2M	US- 6,212,568-B1	04-03-2001	Miller et al.	
u	2N	US- 6,331,905-B1	12-18-2001	Ellinas et al.	
u	2O	US- 6,345,048-B1	02-05-2002	Allen et al.	
u	2P	US- 6,363,334-B1	03-26-2002	Andrews et al.	
u	2Q	US- 6,418,481-B1	07-09-2002	Mancusi et al.	
u	2R	US- 6,442,584-B1	08-27-2002	Kolli et al.	
u	2S	US- 6,452,924-B1	09-17-2002	Golden et al.	

FOREIGN PATENT DOCUMENTS						
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<i>u</i>	3A	US- 6,526,240-B2	02-25-2003	Borowosky et al.	
<i>u</i>	3B	US- 6,539,027-B1	03-25-2003	Cambron	
<i>u</i>	3C	US- 6,539,531-B2	03-25-2003	Miller et al.	
<i>u</i>	3D	US- 6,570,850-B1	05-27-2003	Gutierrez et al.	
<i>u</i>	3E	US- 6,598,080-B1	07-22-2003	Nagami et al.	
<i>u</i>	3F	US- 6,625,777-B1	09-23-2003	Levin et al.	
<i>u</i>	3G	US- 6,633,909-B1	10-14-2003	Barrett et al.	
<i>u</i>	3H	US- 6,650,639-B2	11-18-2003	Doherty et al.	
<i>u</i>	3I	US- 6,668,308-B2	12-23-2003	Barroso et al.	
<i>u</i>	3J	US- 6,675,328-B1	01-06-2004	Krishnarnachari et al.	
<i>u</i>	3K	US- 6,687,222-B1	02-03-2004	Albert et al.	
<i>u</i>	3L	US- 6,694,361-B1	02-17-2004	Shah et al.	
<i>u</i>	3M	US- 6,697,334-B1	02-24-2004	Klincewicz et al.	
<i>u</i>	3N	US- 6,697,369-B1	02-24-2004	Dziong et al.	
<i>u</i>	3O	US- 6,697,854-B1	02-24-2004	Glassen et al.	
<i>u</i>	3P	US- 6,724,757-B1	04-20-2004	Zadikian et al.	
<i>u</i>	3Q	US- 6,744,767-B1	06-01-2004	Chiu et al.	
<i>u</i>	3R	US- 6,757,731-B1	06-29-2004	Barnes et al.	
<i>u</i>	3S	US- 6,778,496-B1	08-17-2004	Meempat et al	

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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date Considered

10/13/85

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NON PATENT LITERATURE DOCUMENTS

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u	5A	Ravindra K. AHUJA et al., Network Flows: Theory, Algorithms, and Applications, pp. 4-9, 649-686, 1993, Prentice-Hall, Upper Saddle River, NJ.	
u	5B	Dimitri P. BERTSEKAS, Linear Network Optimization: Algorithms and Codes, pages 253-260, 1991, The MIT Press, Cambridge, MA.	
u	5C	Robert E. BIXBY, Solving Real-World Linear Programs: A Decade and More of Progress, January 2002.	
u	5D	Viraphol CHAIYAKUL, Assignment Decision Diagram for High-Level Synthesis, Technical Report #92-103, 1992, Department of Information and Computer Science, University of California, Irvine, CA.	
u	5E	Andre DEHON, Notes on Coupling Processors with Reconfigurable Logic, MIT Transit Project, Transit Note #118, 1995.	
u	5F	Julie Ward DREW et al., US Patent Application No. 10/290,760, Filing Date: November 8, 2002.	
u	5G	Julie Ward DREW, US Patent Application No. 10/290,643, Filing Date: November 8, 2002.	
u	5H	Julie Ward DREW et al., US Patent Application No. 10/845,855, Filing Date: May 13, 2004.	
u	5I	Christodoulos A. FLOUDAS et al., Quadratic Optimization, 1995.	
u	5J	Cathy FULTON et al., Impact Analysis of Packet-Level Scheduling On an ATM Shared-Memory Switch, NFOCOM '98. Proceedibgs of Seventeenth Annual Joint Conference of the IEEE Computer and Communications Societies, Vol. 3, pp. 947-954, 1998, IEEE, New York, NY.	

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u	6A	Hiroshi INOSE, An Introduction to Digital Integrated Communication Systems, pages 87-90, 1979, Peter Peregrinus Ltd., Stevenage, United Kingdom.	
u	6B	D. KLINGMAN et al. Netgen: A Program for Generating Large Scale Capacitated Assignment, Transportation, and Minimum Cost Flow Network Problems, Management Science 20(5):814-821, 1974, The Institute Of Management Sciences, Providence, RI.	
u	6C	A. Richard NEWTON, Interface-Based Design; Introduction, University of California at Berkeley, April 1999.	
u	6D	Matthew T. O'KEEFE et al., Designing Fibre Channel Storage Area Networks, February 2001.	
u	6E	Derek C. W. PAO, A Congestion Control Algorithm for Multipoint-to-Multipoint ABR Service in ATM Network, Proceedings of the IEEE Conference on High Performance Switching and Routing, June 26, 2000, pp. 167-175, IEEE, New York, NY.	
u	6F	Parameswaran RAMANATHAN et al., Resource Placement with Multiple Adjacency Constraints in k-ary n-Cubes, 1995.	
u	6G	Staffan Bo STRAND, Storage Area Networks and SANTK, Thesis, December 2001, University of Minnesota, Minneapolis, MN.	
u	6H	Staffan STRAND, Automatic Generation of Core/Edge Topology SANs Using SANTK, May 23, 2002.	
u	6I	Joachim P. WALSER, Solving Linear Pseudo-Boolean Constraint Problems with Local Search, Proceedings of the 14th National Conference on Artificial Intelligence, 1997, pp. 269-274, AAAI Press, Menlo Park, CA.	
u	6J	Julie WARD, US Patent Application No. 09/707,227, Filing Date: November 6, 2000.	

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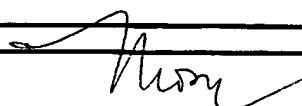
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	7A	Julie WARD et al., Storage Area Network Fabric Design, PowerPoint presentation given at the Math Sciences Research Institute for Combinatorial Design, Berkeley CA, November 8, 2000.	
	7B	I. WIDJAJA et al., Performance Issues in VC-Merge Capable ATM LSRs, RFC 2682, September 1999, The Internet Society, Reston, VA.	

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PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 10019570-1	APPLICATION NO.	CONFIRMATION NO.
	APPLICANT Julie Ward, et al.		
	FILING DATE	GROUP	

J1040 U.S. PTO
 10/02/589
 12/19/01

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>W</i>	1Q	Ward, Julie, et al., "Storage Area Network Fabric Design," PowerPoint presentation given at the Math Sciences Research Institute Conference on Combinatorial Design, Berkeley, California, November 8, 2000.
<i>W</i>	1R	Ward, Julie, et al., "Storage Area Network (SAN) Fabric Design," Powerpoint presentation given at the INFORMS International Conference, Maui, Hawaii, June 19, 2001.
<i>W</i>	1S	Ward, Julie, et al., "Storage Area Network (SAN) Fabric Design," Powerpoint presentation given at Berkeley, IEOR Department Seminar, Berkeley, California, October 8, 2001.

EXAMINER

Ward

DATE CONSIDERED

10/13/05